

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yu ✓
 Title: A PROCESS FOR MANUFACTURING TRANSISTORS HAVING SILICON/GERMANIUM CHANNEL REGIONS ✓
 Appl. No.: 09/599,141 ✓
 Filing Date: 6/22/2000 ✓
 Examiner: unknown
 Art Unit: 2811 ✓



CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on the date below.	
Kristine Lancelle	(Printed Name)
<i>Kristine Lancelle</i>	(Signature)
9/22/00	(Date of Deposit)

H. Coe
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INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner for Patents
 Box
 Washington, D.C. 20231

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Sir:

Pursuant to 37 C.F.R. §§ 1.97-98 and in accordance with the duty of disclosure under 37 C.F.R. §1.56, Applicant submits herewith the references listed on the attached Form PTO-1449 to the Examiner such that they may be considered and made of record in the above-identified application. A copy of each reference is enclosed.

Respectfully submitted,

Date 9-22-00

By *Joseph N. Ziebert*

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